

**Response under 37 CFR 1.116 — Expedited Procedure  
Examining Group 2183**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: Eric F. Vannerson, et al.

Examiner: Huisman, David J

Application No. 10/815,904

Art Unit: 2183

Filed: March 31, 2003

Confirmation No. 9294

For: A Method and Apparatus for  
Multiprocessor Embedded Debugging  
Support Via Attachment of Breakpoint Bit  
Fields to Instructions and Debug Register  
Field Manipulation

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION

Sir:

In response to the Final Office Action mailed August 22, 2008 for the subject patent application, please enter the following amendments and consider the following remarks.